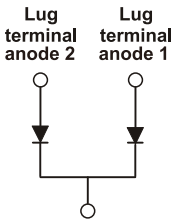
FRED
Ultrafast Soft Recovery Diode Module
200A x 2 / 400V

FEATURES

- Very low Q_{rr} and t_{rr}
- Lead (Pb)-free
- Designed and qualified for industrial level
- Reduced RFI and EMI
- Reduced snubbing
- Planar FRED Chip
- Epoxy molding structure
- 175°C Junction temperature

TO-244M (non-insulated)



DESCRIPTION

FRED diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems .
An extensive characterization of the recovery behavior for different values of current, temperature and dI/dt simplifies the calculations of losses in the operating conditions.The softness of the recovery eliminates the need for a snubber in most applications.

TYPICAL APPLICATIONS

- Power converters
- Motor drives
- Welders
- Switching power supplies
- Uninterruptible power supply (UPS)
- Power factor correction (PFC) circuit
- Inverter
- Choppers
- Battery chargers

PRODUCT SUMMARY	
$I_{F(AV)}$	400A
V_R	400V
$I_{F(DC)}$ at T_C	280A at 76 °C

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		MAX.	UNIT
Cathode to anode voltage	V_R			400	V
Average forward current	$I_{F(AV)}$	T_C =25°C, per leg		400	A
		T_C =107°C	per device	400	
			per leg	200	
Single pulse forward current	I_{FSM}	Limited by junction temperature, per leg		2000	
Non-repetitive avalanche energy	E_{AS}	L = 100 µH, duty cycle limited by maximum T_J		1.4	mJ
Maximum power dissipation	P_D	T_C =25°C		700	W
		T_C =100°C		310	
Operating junction and storage temperature range	T_J, T_{Stg}			- 55 to 175	°C

ELECTRICAL SPECIFICATIONS (T_J = 25 ºC unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Cathode to anode breakdown voltage	V_{BR}	I_R = 100 μA	400	-	-	V
Maximum forward voltage	V_{FM}	I_F = 200 A	-	1.10	1.25	
		I_F = 400 A	-	1.28	1.45	
		I_F = 200 A, T_J = 125 ºC	-	0.95	1.10	
Maximum reverse leakage current per leg	I_{RM}	T_J = 125°C, V_R = 400V	-	2.0	10	mA
		T_J = 25°C, V_R = 400V	-	2.0	10	μA
Junction capacitance	C_T	V_R = 200V	-	460	620	pF
Series inductance	L_S	From top of terminal hole to mounting plane	-	5.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T_J = 25 ºC unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Reverse recovery time	t_{rr}	I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A		-	110	120	ns
		I_F = 1.0A, dI_F/dt=200A/μs, V_R = 30V		-	75	-	
		T_J = 25 ºC	I_F = 200A dI_F/dt = 200 A/μs V_R = 200 V	-	95	-	
		T_J = 125 ºC		-	300	-	
Peak recovery current	I_{RRM}	T_J = 25 ºC		-	9.7	26	A
		T_J = 125 ºC		-	20	34	
Reverse recovery charge	Q_{rr}	T_J = 25 ºC		-	500	1400	nC
		T_J = 125 ºC		-	2900	7700	
Peak rate of recovery current	$dI_{(rec)M}/dt$	T_J = 25 ºC		-	280	-	A/μs
		T_J = 125 ºC		-	260	-	

THERMAL AND MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range		T_J, T_{stg}	-55	-	175	ºC
Thermal resistance, junction to case per leg	TO-244M (non-insulated)	R_{thJC}	-	-	0.15	ºC/W
Thermal resistance, junction to case per module	TO-244M (non-insulated)	R_{thJC}	-	-	0.075	
Typical thermal resistance, case to heatsink		R_{thCS}	-	0.10	-	
Weight	TO-244M (non-insulated)		-	80 (2.82)	-	g (oz.)
Mounting torque(1)			30 (3.4)	-	40 (4.6)	lbf·in (N·m)
Mounting torque center hole			12 (1.4)	-	18 (2.1)	
Terminal torque			30 (3.4)	-	40 (4.6)	
Vertical pull			-	-	80	lbf·in
2″ lever pull			-	-	35	

Note

(1)Mounting surface must be smooth, flat, free of burrs or other protrusions. Apply a thin even film or thermal grease to mounting surface. Gradually tighten each mounting bolt in 5 to 10 lbf. in steps until desired or maximum torque limits are reached

Ordering Information Table

Device code

NK	F	D	400	–	40	M
①	②	③	④		⑤	⑥

1 - Nell's power module
2 - F for Ultrafast soft recovery diode
3 - D for Dual Diodes, TO-244 Package
4 - Maximum average forward current, A
5 - Voltage rating (40 = 400V)
6 - M for Molding structure

Fig.1 Maximum forward voltage drop vs. Instantaneous forward current (per leg)

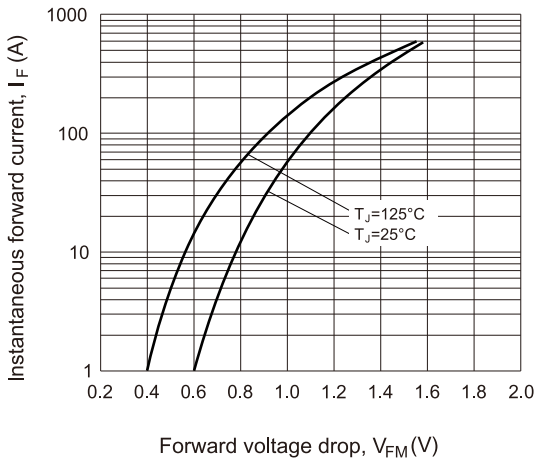


Fig.2 Typical reverse current vs. reverse voltage (per leg)

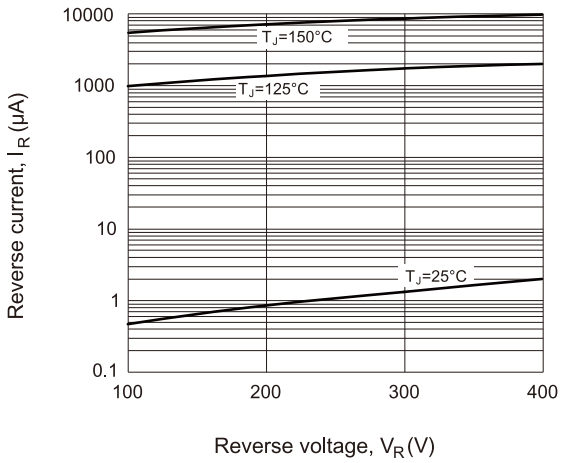


Fig.3 Typical junction capacitance vs. reverse voltage (per leg)

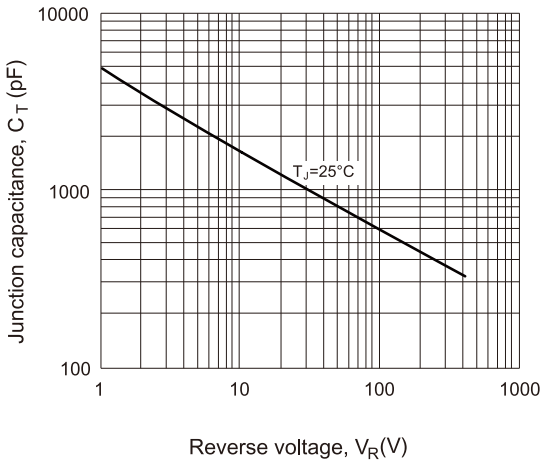


Fig.4 Maximum allowable case temperature vs. DC forward current (per leg)

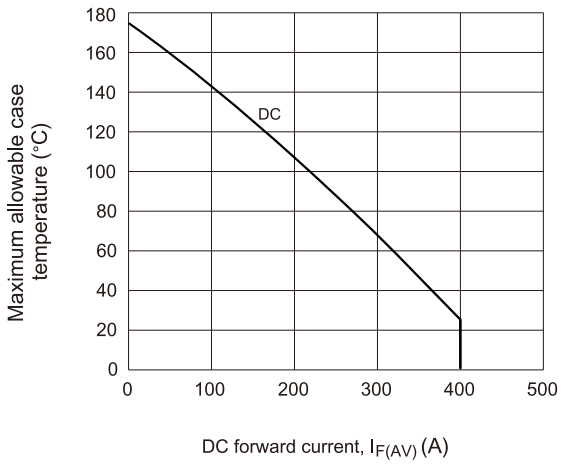


Fig.5 Maximum thermal impedance $R_{th(j-c)}$ characteristics (per leg, for TO-244M non-insulated)

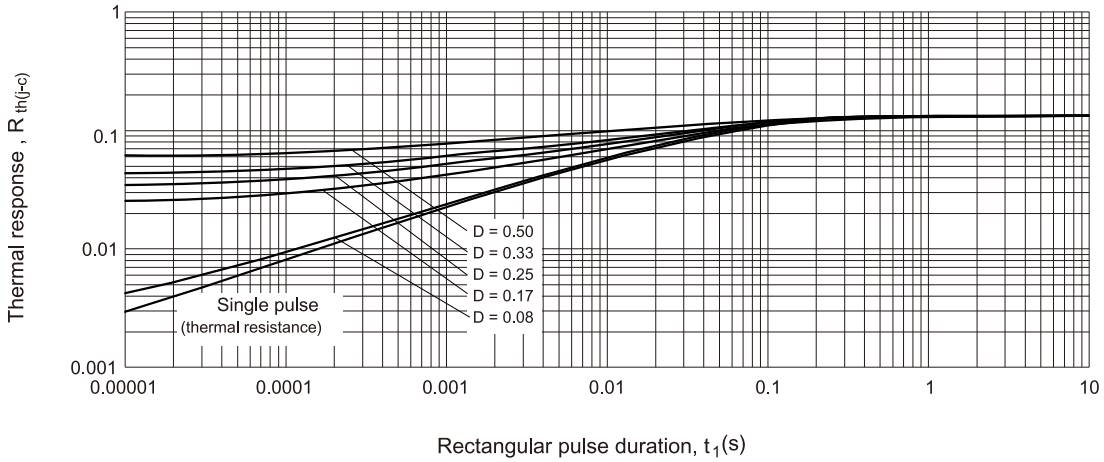


Fig.6 Reverse recovery parameter test circuit

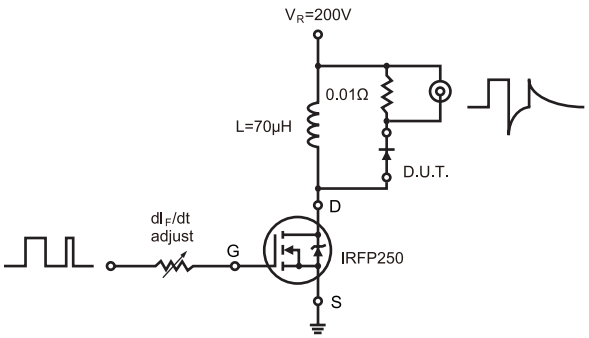
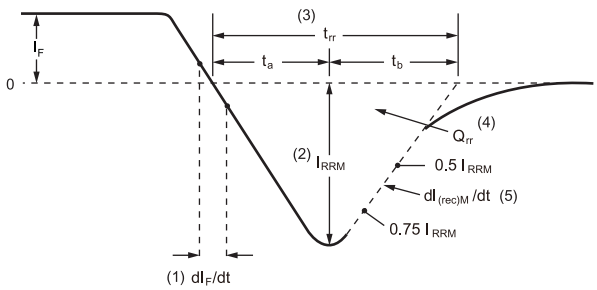


Fig.7 Reverse recovery waveform and definitions



(1) dI_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

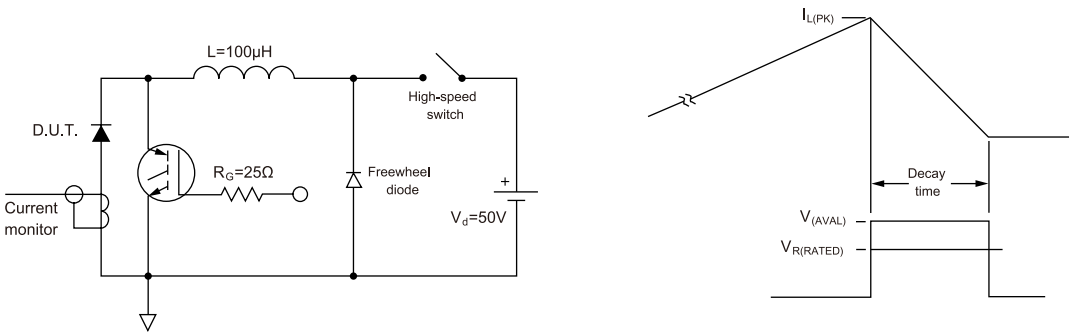
(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

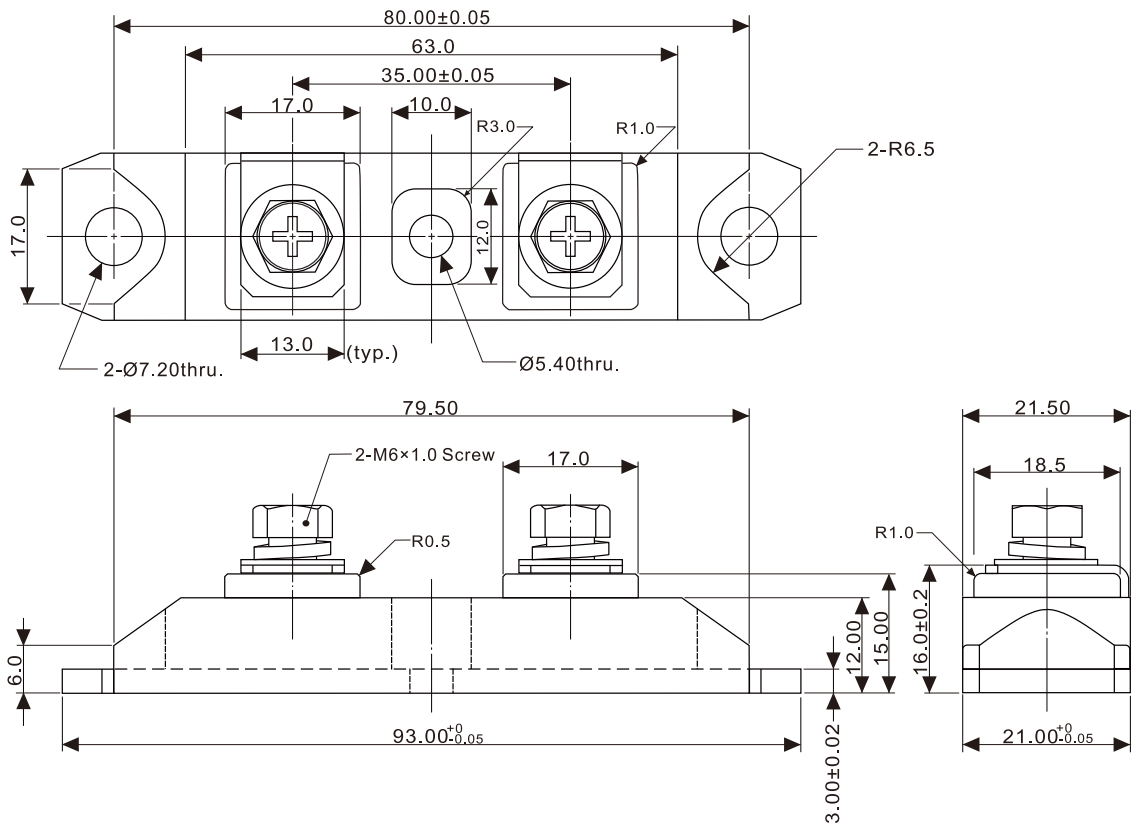
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig.8 Avalanche test circuit and waveforms



TO-244M (Non-Insulated)



All dimensions in millimeters